

Trilithium: an efficient two-party ML-DSA protocol

Peeter Laud

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SplitKey — server-supported RSA signatures

$$\begin{aligned} \text{pk} &= (n_1 n_2, e) \quad n_i = p_i q_i \quad d_i \cdot e \equiv 1 \pmod{\varphi(n_i)} \\ d'_1 + d''_1 &\equiv d_1 \pmod{\varphi(n_1)} \quad u = \text{Enc}(PIN, d'_1) \end{aligned}$$

Client

u, n_1, n_2

Server

d''_1, d_2, n_1, n_2

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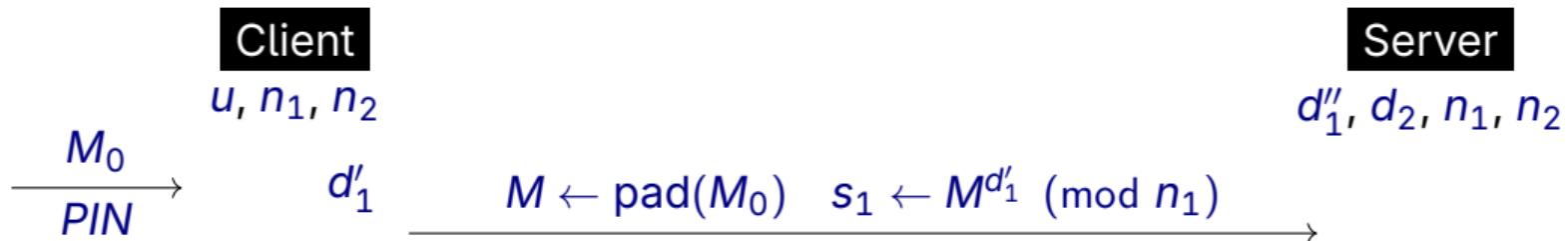
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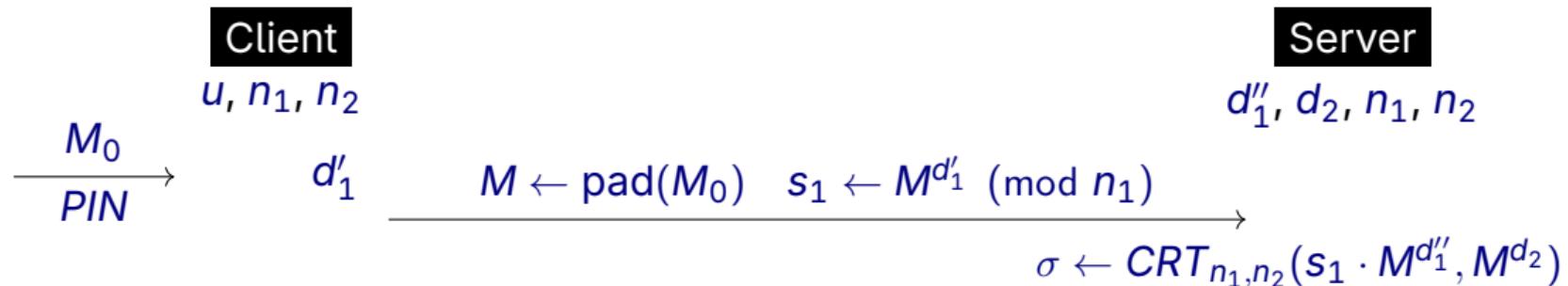
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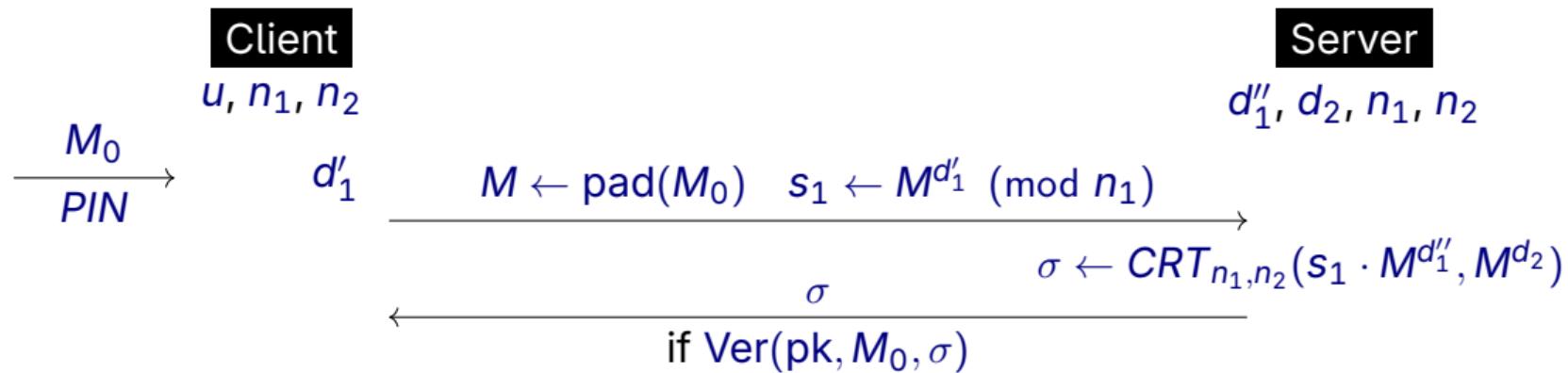
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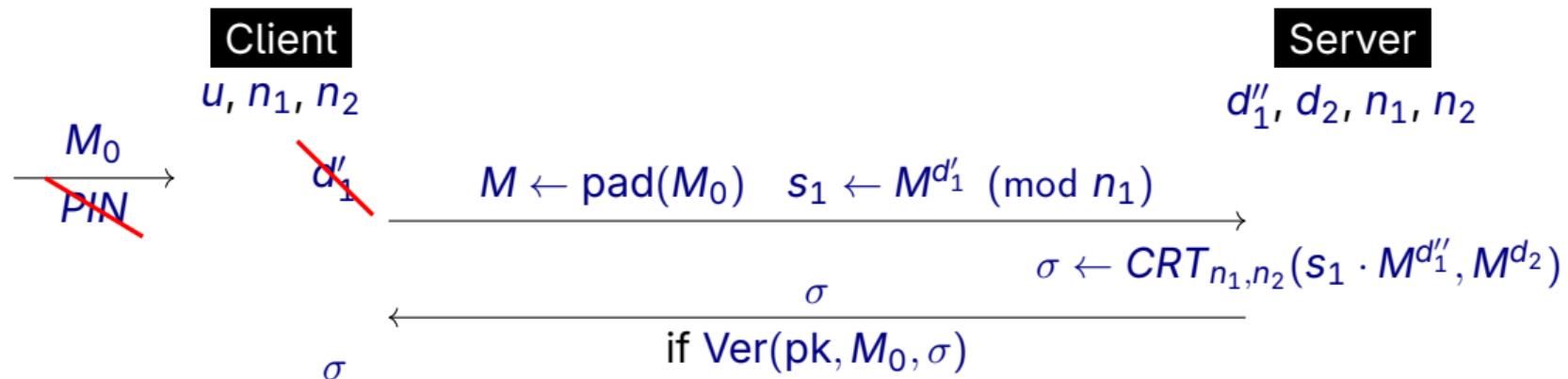
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SplitKey deployment

- Client is implemented in a smartphone
 - If u leaks, then the attacker still cannot brute-force PIN
 - (a *clone detection* mechanism allows Server to reset the wrong PIN counter)
- Produces functionally interchangeable RSA-PSS signatures
 - The length of keys and signatures is double the usual
 - Multi-primality of the modulus is not detected
- Smart-ID service based on SplitKey is running in Baltic states since 2016
 - 6.1M population. 3.7M users. www.smart-id.com
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The days of Splitkey are numbered

Must replace RSA with some postquantum-secure signature scheme

Desiderata

- Two-party protocol
- “SplitKey-like properties”
 - Not in today’s talk. Should not be too difficult
- Standardized
 - At least *interchangeable* with a standardized scheme
- Certifiable
 - Can be meaningfully made to pass the tests created for single-party key generation / signing

Dilithium / ML-DSA details

- $q \in \mathbb{P}$, $q = 2^{23} - 2^{13} + 1 = 2^{13} \cdot 3 \cdot 11 \cdot 31 + 1$. $R_q = \mathbb{Z}_q[X^N + 1]$, $N = 256$
- Think of \mathbb{Z}_q as $\{-\lfloor \frac{q}{2} \rfloor, -\lfloor \frac{q}{2} \rfloor + 1, \dots, -1, 0, 1, \dots, \lfloor \frac{q}{2} \rfloor\}$
 - I.e. each element of \mathbb{Z}_q has size between 0 and $\lfloor \frac{q}{2} \rfloor$
- $\alpha = (q - 1)/44$ or $\alpha = (q - 1)/16$
- “Size” generalizes coefficient- and component-wise to polynomials and tuples. Denote $\|p\|_\infty$, $\|\mathbf{p}\|_\infty$
- $S_\eta := \{p \in R_q \mid \|p\|_\infty \leq \eta\}$
- $B_\tau \subset S_1$; elements of B_τ have exactly τ non-zero coefficients

Let $x' = (x + \frac{\alpha}{2} - 1) \bmod q$
(i.e. $x' \in \{0, \dots, q - 1\}$)

$$x^H := \begin{cases} \lfloor x'/\alpha \rfloor, & x' \neq q - 1 \\ 0, & x' = q - 1 \end{cases}$$

$$x^L := x - \alpha \cdot x^H \pmod{\pm q}$$

Simplified Dilithium / ML-DSA

Signing

- $\mathbf{y} \leftarrow \$ S_{\gamma_1-1}^{\ell}$
- $\mathbf{w} \leftarrow \mathbf{A} \cdot \mathbf{y}$
- $c \leftarrow H(M, \mathbf{w}^H) \in B_{\tau}$
- $\mathbf{z} \leftarrow \mathbf{y} + c \cdot \mathbf{s}_1$
- $\mathbf{x} \leftarrow \mathbf{w}^L - c \cdot \mathbf{s}_2$
- if $\|\mathbf{z}\|_{\infty} \geq \gamma_1 - \beta$, restart
- if $\|\mathbf{x}\|_{\infty} \geq \gamma_2 - \beta$, restart
- return (\mathbf{z}, c)

Key generation

- $\mathbf{A} \leftarrow \$ R_q^{k \times \ell}$ $\mathbf{s}_1 \leftarrow \$ S_{\eta}^{\ell}$ $\mathbf{s}_2 \leftarrow \$ S_{\eta}^k$
- $\mathbf{t} \leftarrow \mathbf{A} \cdot \mathbf{s}_1 + \mathbf{s}_2$
- $\text{pk} = (\mathbf{A}, \mathbf{t})$ $\text{sk} = (\mathbf{s}_1, \mathbf{s}_2)$

Verification

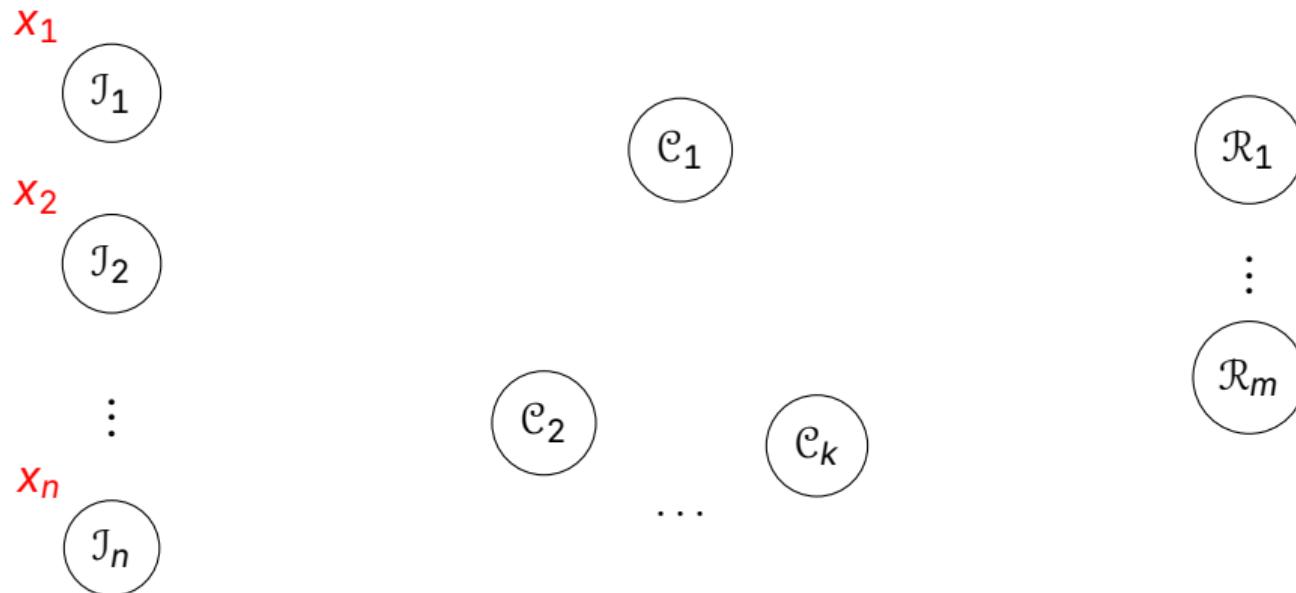
- $\mathbf{w}' \leftarrow \mathbf{A} \cdot \mathbf{z} - c \cdot \mathbf{t}$ **Note that $\mathbf{w}' = \mathbf{x}$**
- Check if $\|\mathbf{z}\|_{\infty} < \gamma_1 - \beta$ and $c = H(M, \mathbf{w}'^H)$

(The *decomposition parameter* α is $2\gamma_2$)

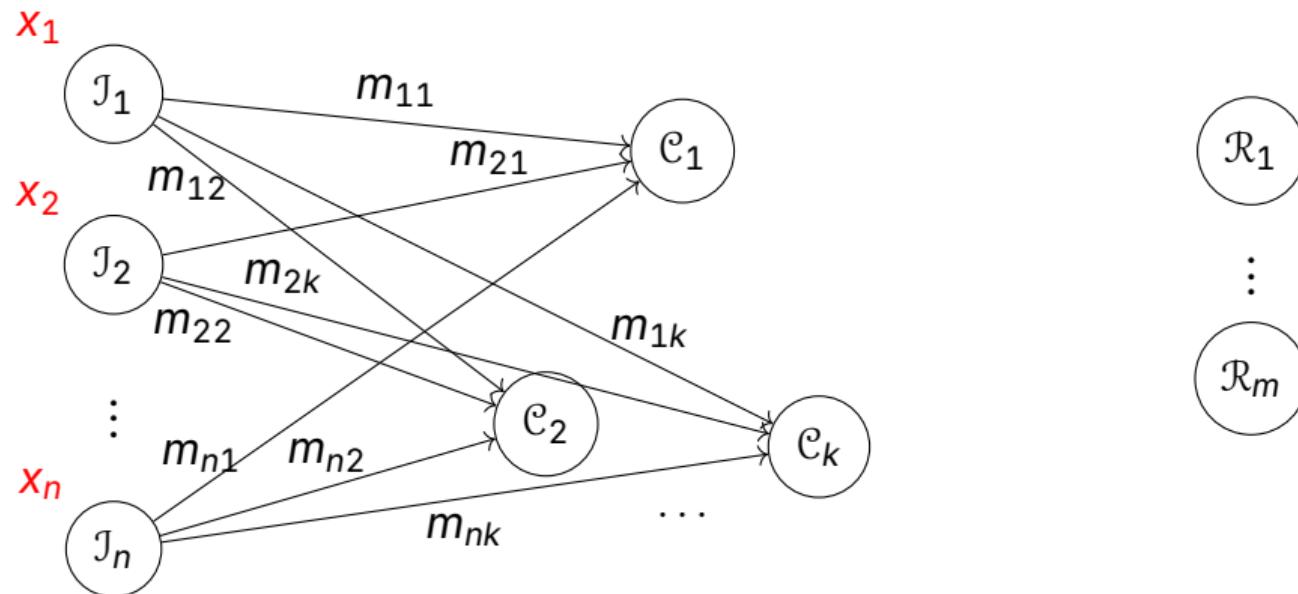
Parameter sizes

| Parameters (see sections 5 and 6 of this document) | Values assigned by each parameter set | | |
|--|---------------------------------------|------------|------------|
| | ML-DSA-44 | ML-DSA-65 | ML-DSA-87 |
| q - modulus [see §5] | 8380417 | 8380417 | 8380417 |
| d - # of dropped bits from \mathbf{t} [see §5] | 13 | 13 | 13 |
| τ - # of ± 1 's in polynomial c [see §6] | 39 | 49 | 60 |
| λ - collision strength of \tilde{c} [see §6] | 128 | 192 | 256 |
| γ_1 - coefficient range of \mathbf{y} [see §6] | 2^{17} | 2^{19} | 2^{19} |
| γ_2 - low-order rounding range [see §6] | $(q-1)/88$ | $(q-1)/32$ | $(q-1)/32$ |
| (k, ℓ) - dimensions of \mathbf{A} [see §5] | (4,4) | (6,5) | (8,7) |
| η - private key range [see §5] | 2 | 4 | 2 |
| $\beta = \tau \cdot \eta$ [see §6] | 78 | 196 | 120 |
| ω - max # of 1's in the hint \mathbf{h} [see §6] | 80 | 55 | 75 |
| Challenge entropy $\log \binom{256}{\tau} + \tau$ [see §6] | 192 | 225 | 257 |
| Repetitions (see explanation below) | 4.25 | 5.1 | 3.85 |

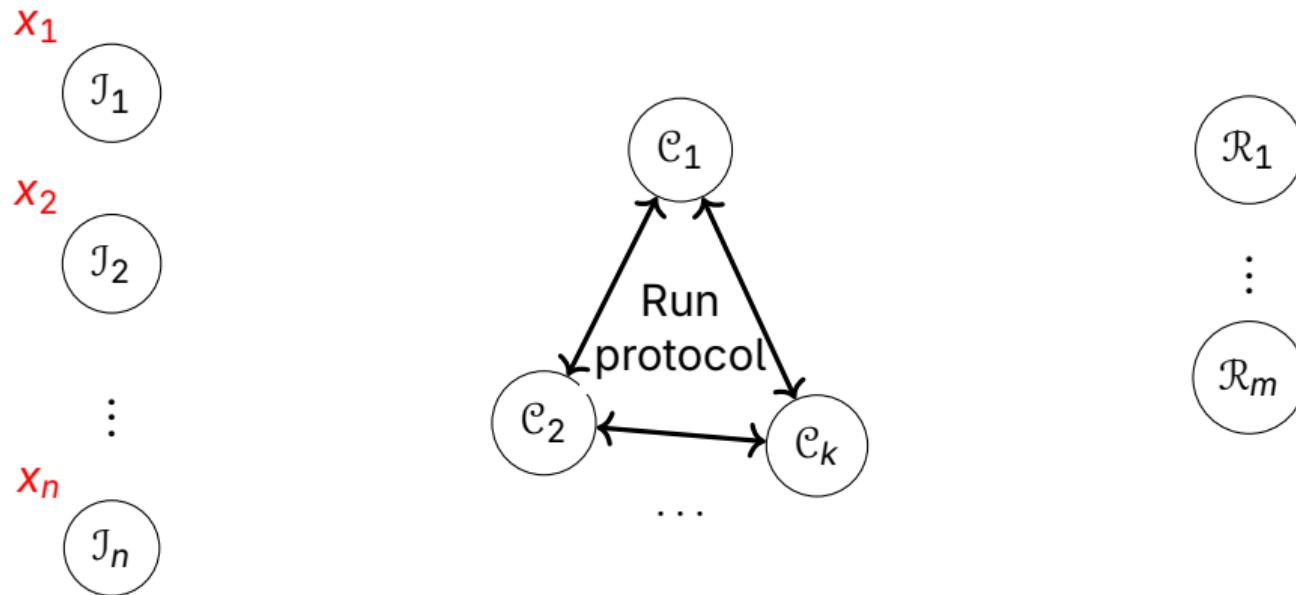
Secure multiparty computation



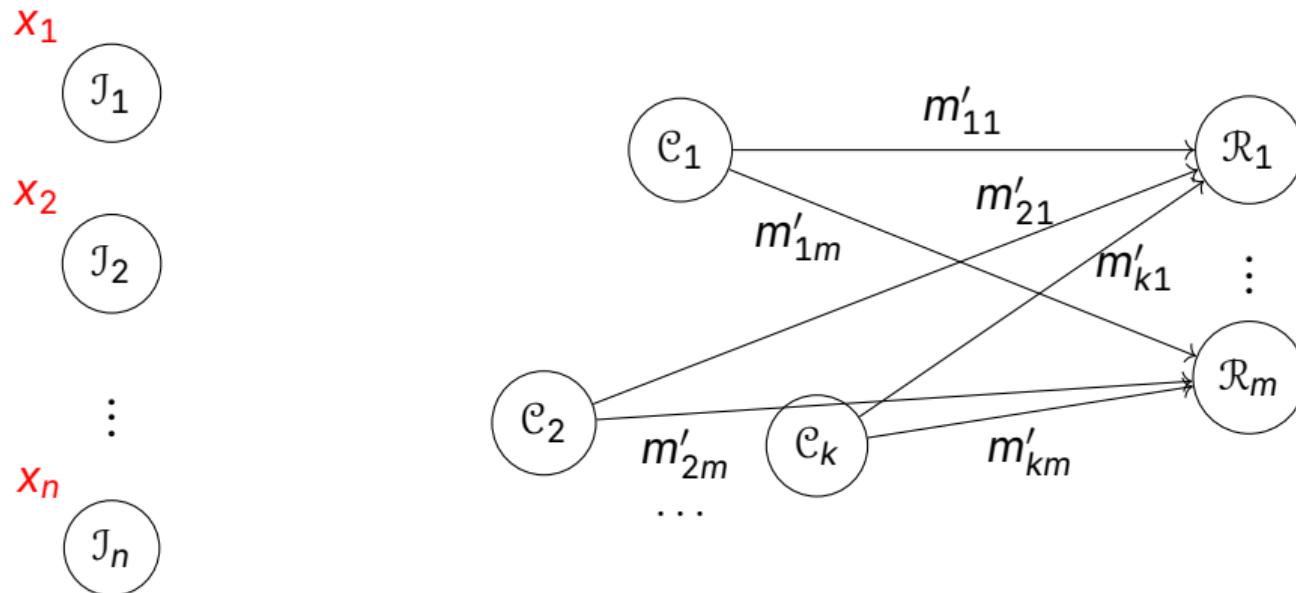
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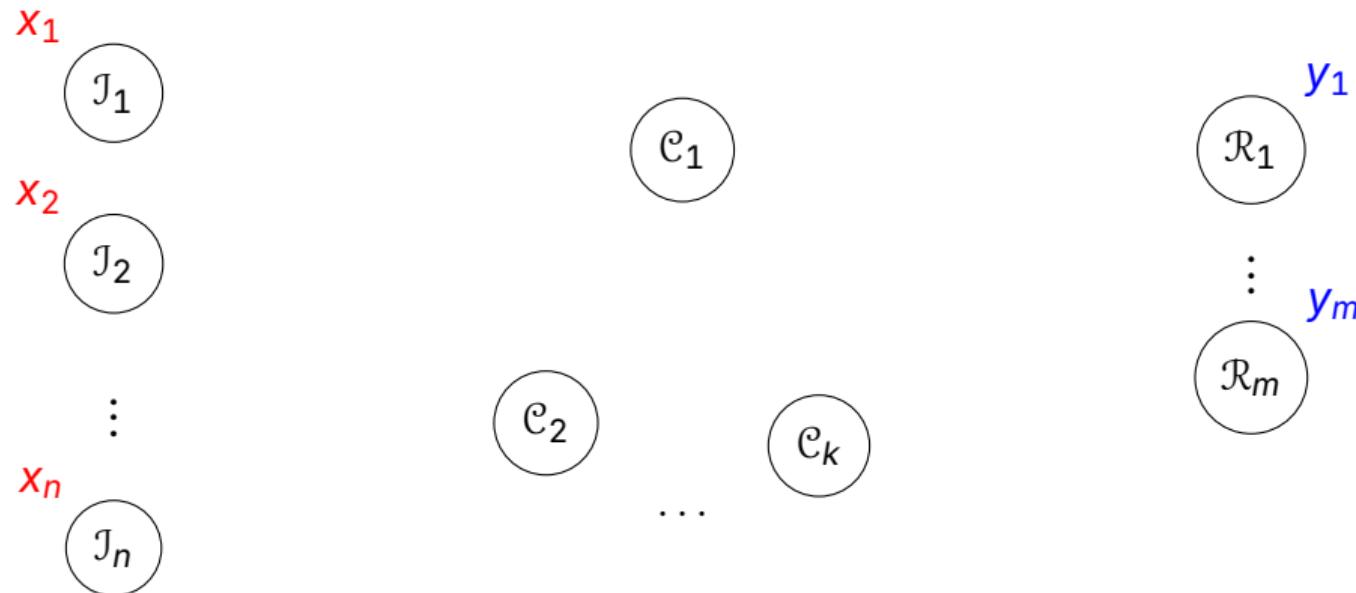
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Secure multiparty computation



Public and private values

Inputs

- Some inputs from \mathcal{I}_i may be **public**
 - It's OK if anyone learns them
 - Think each of m_{i1}, \dots, m_{ik} including such input
- Other inputs from \mathcal{I}_i may be **private**
 - Think a part of m_{i1}, \dots, m_{ik} being a secret sharing of that input
 - \mathcal{I}_i computes that sharing

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Outputs

- Also **public** or **private**
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Computation

- $\mathcal{C}_1, \dots, \mathcal{C}_k$ run a protocol:
 - “shares of inputs” \rightarrow “shares of outputs”
 - no leaks to a single \mathcal{C}_i
 - (or a small set of them)

$\mathcal{C}_1, \dots, \mathcal{C}_k$ as a secure virtual machine

- Think that there's some program computing y_1, \dots, y_m from x_1, \dots, x_n
 - A sequence of three-address operations... e.g. $a_1 \leftarrow \text{mul } a_2 \ a_3$
 - ... where all addresses are public
- For each possible operation, there is a protocol for $\mathcal{C}_1, \dots, \mathcal{C}_k$:
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- Program may branch on public values
 - $a_1 \leftarrow \text{declassify } a_2$ is an available operation

Additive secret sharing

- A **modulus** $q \in \mathbb{Z}$ has been fixed
- The values are elements of \mathbb{Z}_q
 - $\{0, 1, \dots, q - 1\}$, with arithmetic modulo q
- Sharing $v \in \mathbb{Z}_q$ as \mathbf{r} : pick random $r_1, \dots, r_{k-1} \in \mathbb{Z}_q$, let $r_k = v - \sum_{i=1}^{k-1} r_i$
- Recovery of v from \mathbf{r} : compute $v \leftarrow r_1 + \dots + r_k$
 - ...modulo q

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 - ...modulo q
- Let $\llbracket v \rrbracket$ denote “ v that is held shared among $\mathcal{C}_1, \dots, \mathcal{C}_k$ ”
 - Let $\llbracket v \rrbracket_i$ denote the share held by \mathcal{C}_i

Linear computations

- Given shared values $\llbracket x \rrbracket, \llbracket y \rrbracket$, computing parties can compute:
 - $\llbracket x + y \rrbracket, \llbracket x - y \rrbracket$
 - $\llbracket c \cdot x \rrbracket$ for some constant $c \in \mathbb{Z}_q$
- These can be computed without the computing parties talking to each other
 - Computing party does the operations locally, on its share(s)
- Can also locally create $\llbracket c \rrbracket$ for a constant $c \in \mathbb{Z}_q$

Multiplication as a non-linear computation

- Have $\llbracket x \rrbracket, \llbracket y \rrbracket$. Want $\llbracket z \rrbracket$, where $z = xy \pmod{q}$
- Suppose computing parties have obtained $\llbracket a \rrbracket, \llbracket b \rrbracket, \llbracket c \rrbracket$, such that
 - $a, b \in \mathbb{Z}_q$ are generated uniformly randomly. $c = ab$
 - $\llbracket a \rrbracket, \llbracket b \rrbracket, \llbracket c \rrbracket$ have not been used anywhere in the computation

Protocol

- Compute $\llbracket \alpha \rrbracket = \llbracket x \rrbracket - \llbracket a \rrbracket$ and $\llbracket \beta \rrbracket = \llbracket y \rrbracket - \llbracket b \rrbracket$
- **Declassify** α and β . (This is privacy-preserving)
- Compute $\llbracket z \rrbracket \leftarrow \alpha \cdot \llbracket y \rrbracket + \beta \cdot \llbracket a \rrbracket + \llbracket c \rrbracket$

Creating $\llbracket a \rrbracket, \llbracket b \rrbracket, \llbracket c \rrbracket$

- An *offline* protocol, or
- a separate “Correlated Randomness Provider” (CRP) party

A typical pattern of MPC protocols

1. Compute some linear combinations of shared values
 - with constants that all computing parties know
2. Declassify some values
 - These become constants that all computing parties know
 - Make sure that declassification is privacy-preserving
 - E.g. they have been masked with randomness not used elsewhere
3. GOTO 1

Passive vs. active corruptions

- An (external) adversary may “corrupt” the parties
 - The protocol is secure as long as not too many parties are corrupted
- A passively corrupted party follows the protocol, but reports all it sees to the adversary
- An actively corrupted party also gets from the adversary the messages it sends to other parties
- There are surely contexts where *passively secure* MPC makes sense
 - Threshold computation of signatures is not one of these contexts
 - We'll come back to it later

Easy and hard parts of signing under MPC

Signing

- $\mathbf{y} \leftarrow \$S_{\gamma_1-1}^{\ell}$
- $\mathbf{w} \leftarrow \mathbf{A} \cdot \mathbf{y}$
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- return (\mathbf{z}, c)

Set-up

- Additive secret sharing modulo q
- \mathbf{w}^H is declassified. c computed in clear

How?

- Green — linear computations
- Blue — we know how to do
 - Elements of \mathbf{y} are between -2^{γ_1-1} and $2^{\gamma_1-1} - 1$
 - Can generate random shared bits modulo q
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Intro to high bits: bit decomposition

Task

Given $\llbracket v \rrbracket$ (modulo q), find $\llbracket v_0 \rrbracket, \dots, \llbracket v_{n-1} \rrbracket$ ($2^n > q$), such that:

- $v_0, \dots, v_{n-1} \in \{0, 1\}$
- $\sum_{i=0}^{n-1} 2^i v_i = v$

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Correlated randomness

$\llbracket r \rrbracket, \llbracket r_0 \rrbracket, \dots, \llbracket r_{n-1} \rrbracket$

- $r \in \mathbb{Z}_q$ is random; rest is like v -s

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Protocol

- Compute $y \leftarrow \text{declassify}(\llbracket v \rrbracket - \llbracket r \rrbracket)$
- Execute bitwise addition circuit:

$$\begin{array}{cccc} \llbracket r_{n-1} \rrbracket & \cdots & \llbracket r_1 \rrbracket & \llbracket r_0 \rrbracket \\ \hline y_{n-1} & \cdots & y_1 & y_0 \\ \hline \llbracket v_{n-1} \rrbracket & \cdots & \llbracket v_1 \rrbracket & \llbracket v_0 \rrbracket \end{array}$$

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Result

$$\sum_{i=0}^{n-1} 2^i v_i \in \{v, v + q\}$$

More general intro: characteristic vectors

- The characteristic vector of $v \in \{0, 1, \dots, U - 1\}$ is $(\underbrace{0, \dots, 0}_{v \text{ times}}, 1, \underbrace{0, \dots, 0}_{(U-v-1) \text{ times}})$
- Sometimes we represent a private v as $\llbracket v_0 \rrbracket, \dots, \llbracket v_{U-1} \rrbracket$
 - Only if U is not too large
- This linearizes *all* functions $f : \{0, 1, \dots, U - 1\} \rightarrow Y$:

$$\llbracket f(v) \rrbracket = \sum_{i=0}^{U-1} f(i) \cdot \llbracket v_i \rrbracket$$

Computing the characteristic vector

Task

Given $\llbracket v \rrbracket$ (modulo q), find

$\llbracket v_0 \rrbracket, \dots, \llbracket v_{q-1} \rrbracket$, such that:

- v_0, \dots, v_{q-1} is the C.V. of v

(Note that length of vector equals sharing modulus)

Computing the characteristic vector

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Protocol

- Compute $y \leftarrow \text{declassify}(\llbracket v \rrbracket - \llbracket r \rrbracket)$
- Rotate r , so it becomes v :

$$\llbracket v_i \rrbracket \leftarrow \llbracket r_{(i-y) \bmod q} \rrbracket \quad \text{for all } i$$

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Moduli of sharing

$\llbracket v \rrbracket$ may be shared over a modulus different from $\llbracket v_0 \rrbracket, \dots, \llbracket v_{q-1} \rrbracket$

Decomposition into digits

- Given (constants) $(r_0, r_1, \dots, r_{n-1}) \in \mathbb{Z}^n$ be such, that $\prod_i r_i \geq 2q$
 - Denote $R_i = r_0 \cdot r_1 \cdots r_{i-1}$. (And $R_0 = 1$)
- Given $\llbracket v \rrbracket$, compute $\llbracket d_0 \rrbracket, \dots, \llbracket d_{n-1} \rrbracket$, such that
 - $0 \leq d_i < r_i$
 - $\sum_{i=0}^{n-1} R_i \cdot d_i \in \{v, v + q\}$
- Actually, instead of $\llbracket d_i \rrbracket$ output its C.V. $\llbracket \mathbf{b}_i \rrbracket$

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- Actually, instead of $\llbracket d_i \rrbracket$ output its C.V. $\llbracket \mathbf{b}_i \rrbracket$
- Protocol: very similar to bit decomposition
 - Correlated randomness: shares of $r \leftarrow \mathbb{Z}_q$ and C.V.-s of r 's digits
 - Addition circuit is similar; C.V.-s help to compute the carries

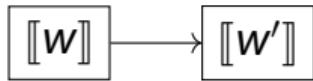
Computing w^H

$$w' \leftarrow (w + \frac{\alpha}{2} - 1) \pmod{q}$$

$$w^H = \begin{cases} \lfloor w'/\alpha \rfloor, & w' < q - 1 \\ 0, & w' = q - 1 \end{cases}$$

Computing high bits

Computing high bits



Computing w^H

$$w' \leftarrow (w + \frac{\alpha}{2} - 1) \pmod{q}$$

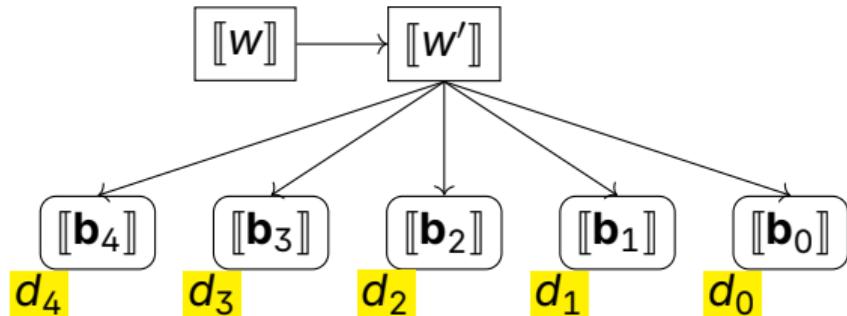
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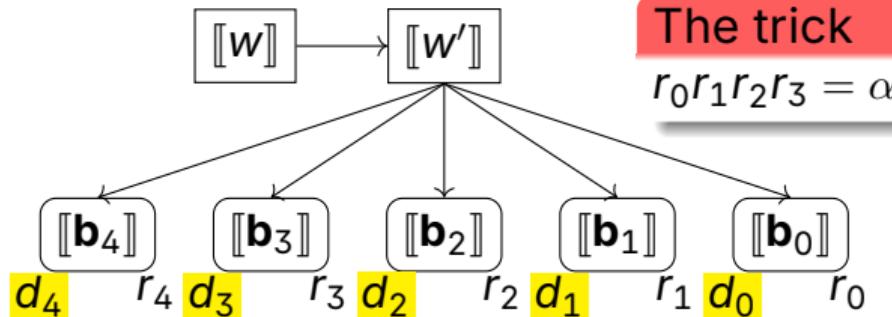
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Computing high bits



Computing high bits



Computing w^H

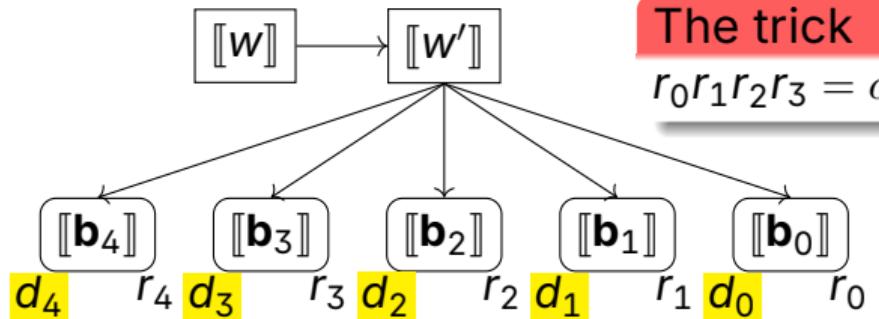
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$$w^H = \begin{cases} \lfloor w'/\alpha \rfloor, & w' < q - 1 \\ 0, & w' = q - 1 \end{cases}$$

Denote $s = (q - 1)/\alpha$

Denote $D = \sum_i R_i d_i$

Computing high bits



If $d_4 < s$

(then $D = w' < q - 1$)

Computing w^H

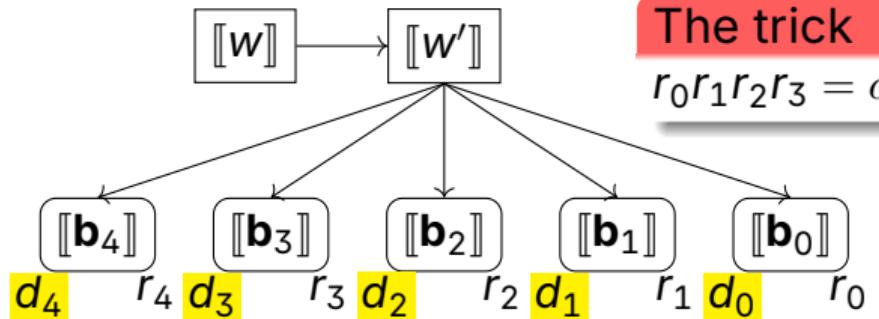
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Denote $s = (q - 1)/\alpha$

Denote $D = \sum_i R_i d_i$

Computing high bits



If $d_4 < s$

(then $D = w' < q - 1$)
return $\llbracket d_4 \rrbracket$

Computing w^H

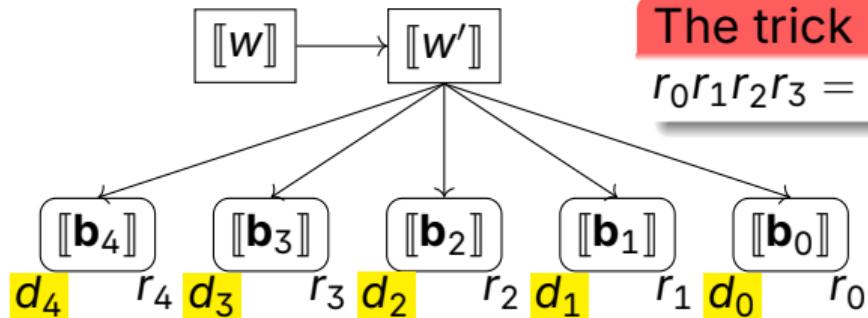
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Denote $s = (q - 1)/\alpha$

Denote $D = \sum_i R_i d_i$

Computing high bits



If $d_4 < s$
(then $D = w' < q - 1$)
return $\llbracket d_4 \rrbracket$

Otherwise
 $(D = w' + q \text{ (or } w' = q - 1\text{)})$

Computing w^H

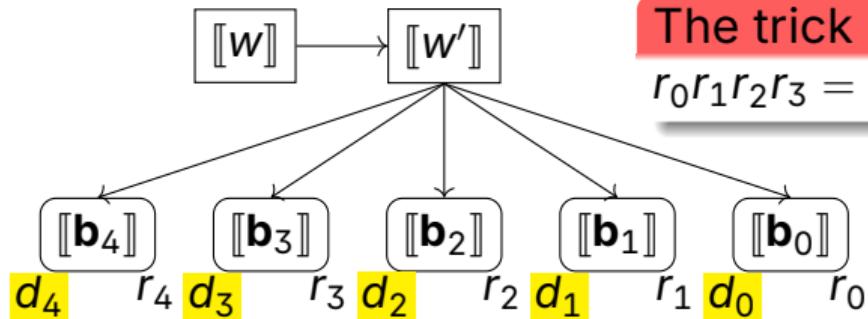
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Denote $s = (q - 1)/\alpha$

Denote $D = \sum_i R_i d_i$

Computing high bits



If $d_4 < s$
(then $D = w' < q - 1$)
return $[d_4]$

Otherwise
($D = w' + q$ (or $w' = q - 1$))
return $[d_4] - s$
(subtracts $q - 1$ from D)

Computing w^H

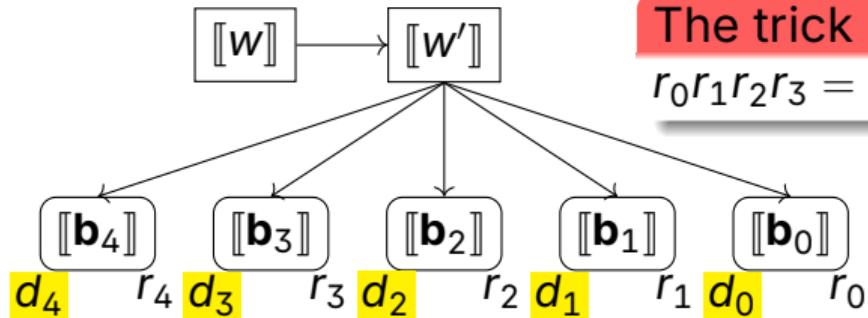
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Denote $s = (q - 1)/\alpha$

Denote $D = \sum_i R_i d_i$

Computing high bits



If $d_4 < s$

(then $D = w' < q - 1$)
return $\llbracket d_4 \rrbracket$

Otherwise

$(D = w' + q \text{ (or } w' = q - 1\text{)})$
return $\llbracket d_4 \rrbracket - s - \llbracket C \rrbracket$
(subtracts $q - 1$ from D)

Computing w^H

$$w' \leftarrow (w + \frac{\alpha}{2} - 1) \pmod{q}$$

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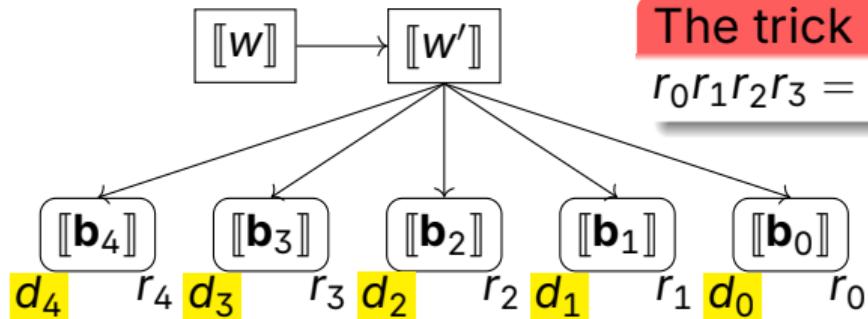
Denote $s = (q - 1)/\alpha$

Denote $D = \sum_i R_i d_i$

Condition C

$$d_0, \dots, d_3 = 0$$

Computing high bits



If $d_4 < s$

(then $D = w' < q - 1$)
return $\llbracket d_4 \rrbracket$

Otherwise

$(D = w' + q \text{ (or } w' = q - 1\text{)})$
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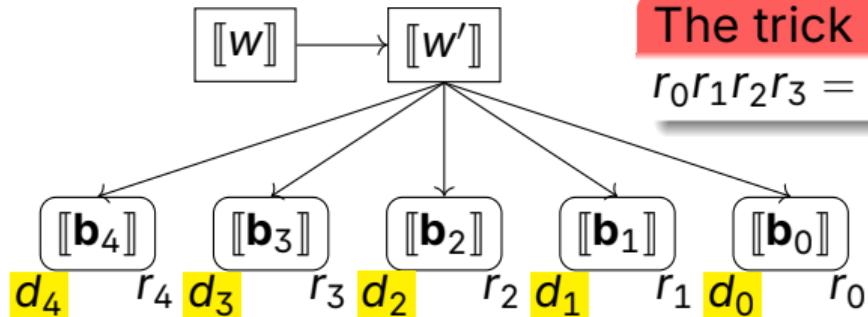
Denote $s = (q - 1)/\alpha$

Denote $D = \sum_i R_i d_i$

Condition C

$$d_0, \dots, d_3 = 0, d_4 > s$$

Computing high bits



If $d_4 < s$

(then $D = w' < q - 1$)
return $\llbracket d_4 \rrbracket$

Otherwise

$(D = w' + q \text{ (or } w' = q - 1\text{)})$
return $\llbracket d_4 \rrbracket - s - \llbracket C \rrbracket$
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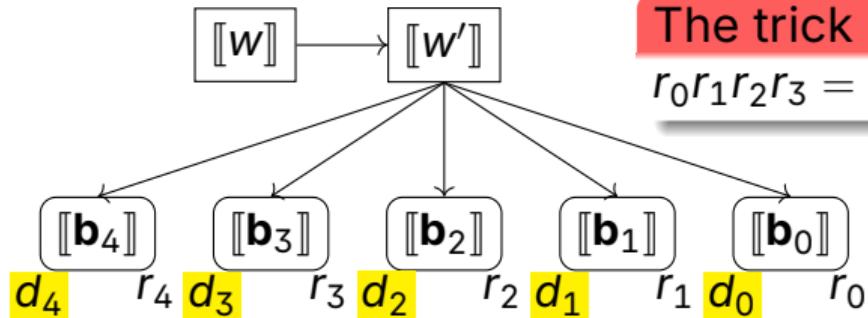
Denote $D = \sum_i R_i d_i$

Condition C

$$d_0, \dots, d_3 = 0, d_4 > s$$

$$b_{0,0}, \dots, b_{3,0}, (\sum_{i>s} b_{4,i}) = 1$$

Computing high bits



If $d_4 < s$

(then $D = w' < q - 1$)
return $\llbracket d_4 \rrbracket$

Otherwise

$(D = w' + q \text{ (or } w' = q - 1\text{)})$
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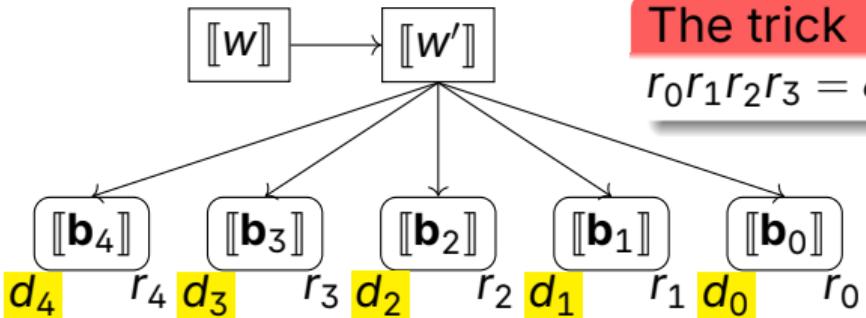
Condition C

$$d_0, \dots, d_3 = 0, d_4 > s$$

$$b_{0,0}, \dots, b_{3,0}, (\sum_{i>s} b_{4,i}) = 1$$

- Each is $\in \{0, 1\}$
- Add up, check that = 5

Computing high bits



The computation from $\llbracket b_i \rrbracket$ is:

- Let $f(x) := \mathbf{if } x < s \mathbf{ then } x \mathbf{ else } x - s$
- Compute $\llbracket C \rrbracket$, evaluating \rightarrow under $\llbracket \cdot \rrbracket$
- **return** $\llbracket f(d_4) \rrbracket - \llbracket C \rrbracket$

Computing w^H

$$w' \leftarrow (w + \frac{\alpha}{2} - 1) \pmod{q}$$

$$w^H = \begin{cases} \lfloor w'/\alpha \rfloor, & w' < q - 1 \\ 0, & w' = q - 1 \end{cases}$$

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Limited range zero check

Task

- Given $\llbracket v \rrbracket$ (modulo q) with $v \leq B$
- Compute $\llbracket b \rrbracket$, $b = [v = 0]$

Limited range zero check

Task

- Given $\llbracket v \rrbracket$ (modulo q) with $v \leq B$
- Compute $\llbracket b \rrbracket$, $b = [v = 0]$

Denote $a = \lfloor q/B \rfloor$

Correlated randomness

- $\llbracket r \rrbracket, \llbracket \mathbf{t} \rrbracket, r \leftarrow \mathbb{Z}_q$
- \mathbf{t} is C.V. of $\lfloor r/a \rfloor$
 - ... of length B

Limited range zero check

Task

- Given $\llbracket v \rrbracket$ (modulo q) with $v \leq B$
- Compute $\llbracket b \rrbracket$, $b = [v = 0]$

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Correlated randomness

- $\llbracket r \rrbracket, \llbracket t \rrbracket, r \leftarrow \mathbb{Z}_q$
- t is C.V. of $\lfloor r/a \rfloor$
 - ... of length B

Protocol

- $y \leftarrow \text{declassify}(\llbracket r \rrbracket + a \cdot \llbracket v \rrbracket)$
- Output $\llbracket t_{\lfloor y/a \rfloor} \rrbracket$

Limited range zero check

Task

- Given $\llbracket v \rrbracket$ (modulo q) with $v \leq B$
- Compute $\llbracket b \rrbracket$, $b = [v = 0]$

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Correlated randomness

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Protocol

- $y \leftarrow \text{declassify}(\llbracket r \rrbracket + a \cdot \llbracket v \rrbracket)$
- Output $\llbracket t_{\lfloor y/a \rfloor} \rrbracket$

Correctness

Must get $\lfloor \frac{r}{a} \rfloor = \lfloor \frac{y}{a} \rfloor$ iff $v = 0$

- $\lfloor \frac{y}{a} \rfloor = \lfloor \frac{r+av}{a} \rfloor = \lfloor \frac{r}{a} \rfloor + v$
- But if $r + av \geq q$ then $y \leq r - a$ and $\lfloor \frac{y}{a} \rfloor \leq \lfloor \frac{r}{a} \rfloor - 1$

Computing whether $\llbracket w \rrbracket < c$

- Say that $\llbracket w \rrbracket$ overflows, if $\llbracket w \rrbracket_0 + \llbracket w \rrbracket_1 \geq q$
 - Let there be a protocol $OF(\llbracket w \rrbracket) \mapsto \llbracket b \rrbracket$
- Let c be shared as $\llbracket c \rrbracket_0 = 0, \llbracket c \rrbracket_1 = c$
- Let
 - $\llbracket X \rrbracket = OF(\llbracket w \rrbracket)$
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| X | Y | Z | R |
|-----|-----|-----|-----|
| 0 | 0 | 0 | |
| 0 | 0 | 1 | |
| 0 | 1 | 0 | |
| 0 | 1 | 1 | |
| 1 | 0 | 0 | |
| 1 | 0 | 1 | |
| 1 | 1 | 0 | |
| 1 | 1 | 1 | |

First row in table

Computing whether $\llbracket w \rrbracket <$

- $0 \leq \llbracket w \rrbracket_0 + \llbracket w \rrbracket_1 < q$
- $0 \leq \llbracket w \rrbracket_0 + (\llbracket w \rrbracket_1 - c) \bmod q < q$
- $0 \leq \llbracket w \rrbracket_1 < c$

- Say that $\llbracket w \rrbracket$ overflows, if $\llbracket w \rrbracket_0 + \llbracket w \rrbracket_1 \geq q$
 - Let there be a protocol $OF(\llbracket w \rrbracket) \mapsto \llbracket b \rrbracket$
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| X | Y | Z | R |
|-----|-----|-----|-----|
| 0 | 0 | 0 | |
| 0 | 0 | 1 | |
| 0 | 1 | 0 | |
| 0 | 1 | 1 | |
| 1 | 0 | 0 | |
| 1 | 0 | 1 | |
| 1 | 1 | 0 | |
| 1 | 1 | 1 | |

First row in table

- $0 \leq \llbracket w \rrbracket_0 + \llbracket w \rrbracket_1 < q$
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- $0 \leq \llbracket w \rrbracket_1 < c$
- $0 \leq \llbracket w \rrbracket_0 + \llbracket w \rrbracket_1 + q - c < q$
- $c - q \leq \llbracket w \rrbracket_0 + \llbracket w \rrbracket_1 < c$

| | | |
|---|---|---|
| 0 | 0 | 1 |
| 0 | 1 | 0 |
| 0 | 1 | 1 |
| 1 | 0 | 0 |
| 1 | 0 | 1 |
| 1 | 1 | 0 |
| 1 | 1 | 1 |

Computing whether $\llbracket w \rrbracket <$

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Computing whether $\llbracket w \rrbracket < c$

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| X | Y | Z | R |
|-----|-----|-----|-----|
| 0 | 0 | 0 | 0 |
| 0 | 0 | 1 | |
| 0 | 1 | 0 | |
| 0 | 1 | 1 | |
| 1 | 0 | 0 | |
| 1 | 0 | 1 | |
| 1 | 1 | 0 | |
| 1 | 1 | 1 | |

Computing whether $\llbracket w \rrbracket < c$

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| X | Y | Z | R |
|-----|-----|-----|---------|
| 0 | 0 | 0 | 0 |
| 0 | 0 | 1 | 1 |
| 0 | 1 | 0 | 1 |
| 0 | 1 | 1 | \perp |
| 1 | 0 | 0 | \perp |
| 1 | 0 | 1 | 0 |
| 1 | 1 | 0 | 0 |
| 1 | 1 | 1 | 1 |

Computing whether $\llbracket w \rrbracket < c$

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 - $\llbracket Z \rrbracket_0 = 0, \llbracket Z \rrbracket_1 \in \{0, 1\}, \llbracket Z \rrbracket_1 = 1$ iff $\llbracket w \rrbracket_1 \geq c$
- Output $\llbracket R \rrbracket = \llbracket Y \rrbracket - \llbracket X \rrbracket + \llbracket Z \rrbracket$

| X | Y | Z | R |
|-----|-----|-----|---------|
| 0 | 0 | 0 | 0 |
| 0 | 0 | 1 | 1 |
| 0 | 1 | 0 | 1 |
| 0 | 1 | 1 | \perp |
| 1 | 0 | 0 | \perp |
| 1 | 0 | 1 | 0 |
| 1 | 1 | 0 | 0 |
| 1 | 1 | 1 | 1 |

Computing overflow of $\llbracket w \rrbracket$ (small sharing modulus r)

- Let $\llbracket u \rrbracket_0 \leftarrow \llbracket w \rrbracket_0$ and $\llbracket u \rrbracket_1 \leftarrow \llbracket v \rrbracket_1$
- Think of $\llbracket u \rrbracket$ being shared modulo q , where $q \geq 2r - 1$
- Let $\llbracket v \rrbracket$ be the C.V. of $\llbracket u \rrbracket$
- Compute $\llbracket c \rrbracket \leftarrow \sum_{i=r}^{q-1} \llbracket v_i \rrbracket$
- (The next algorithm also needs $\llbracket b \rrbracket \leftarrow \llbracket v_{r-1} \rrbracket$)

Computing overflow of $\llbracket w \rrbracket$

- Fix a radix basis (r_0, \dots, r_{n-1}) , s.t. $R_n > q$
- Also fix moduli q_0, \dots, q_{n-1} , s.t. $q_i \geq 2r_i - 1$
- Party 0 splits $\llbracket w \rrbracket_0$ to digits $\llbracket v_0 \rrbracket_0, \dots, \llbracket v_{n-1} \rrbracket_0$
- Party 1 splits $\llbracket w \rrbracket_1 + (R_n - q)$ to digits $\llbracket v_0 \rrbracket_1, \dots, \llbracket v_{n-1} \rrbracket_1$
- Compute overflow bits $\llbracket b_i \rrbracket, \llbracket c_i \rrbracket$ for $\llbracket v_i \rrbracket$. Result is

$$c_{n-1} \vee (c_{n-2} \wedge b_{n-1}) \vee (c_{n-3} \wedge b_{n-2} \wedge b_{n-1}) \vee \dots \vee (c_0 \wedge b_1 \wedge \dots \wedge b_{n-1})$$

- This can be privately computed as follows...

Computing that boolean formula

- $c_{n-1} \vee (c_{n-2} \wedge b_{n-1}) \vee (c_{n-3} \wedge b_{n-2} \wedge b_{n-1}) \vee \cdots \vee (c_0 \wedge b_1 \wedge \cdots \wedge b_{n-1})$
 - Note that $b_i \wedge c_i = \text{false}$ for all i
 - Hence at most one disjunct is true
- $\llbracket b_i \rrbracket, \llbracket c_i \rrbracket$ are additively shared. We do not have " \wedge " and " \vee ".
- Define *weights*: $\gamma_i \leftarrow 2^i$. $\beta_0 \leftarrow 0$. $\beta_i \leftarrow 2^{i-1}$
- Compute $\llbracket S \rrbracket \leftarrow \sum_{i=0}^{n-1} \gamma_i \cdot \llbracket c_i \rrbracket + \sum_{i=0}^{n-1} \beta_i \cdot \llbracket b_i \rrbracket$
 - Let the sharing modulus be $\geq 2^n$. Then the computation does not overflow
- $\llbracket S \rrbracket \geq 2^{n-1}$ iff the boolean formula is true
- Use the C.V. of $\llbracket S \rrbracket$ to find out

Security against active adversaries

- How does one party verify that the other party is following the protocol?
- “Active security”: a deviation will be caught
 - A wrong answer will be impossible
 - leaks due to deviation cannot happen
- “Active privacy”: Nothing leaks to the deviating party
 - ...until the result has been opened
 - the amount of “wrongness” in the result may give information to the malicious party
- The (sub-)protocols we've seen so far only provide security against passive adversaries

BeDOZA - style MACs

- Let the sharing be over a *field* \mathbb{Z}_q
- P_i has a private *MAC key* $\Delta_i \in \mathbb{Z}_q$
- The sharing $\langle\!\langle x \rangle\!\rangle$ of $x \in \mathbb{Z}_q$ consists of
 - $\llbracket x \rrbracket$ (as in passive case)
 - $\llbracket \llbracket x \rrbracket_0 \cdot \Delta_1 \rrbracket$ and $\llbracket \llbracket x \rrbracket_1 \cdot \Delta_0 \rrbracket$ (the “MAC”-s)
- Whenever P_i sends some $\llbracket y \rrbracket_i$ to P_j , it also sends $\llbracket \llbracket y \rrbracket_i \cdot \Delta_j \rrbracket_i$

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- Whenever P_i sends some $\llbracket y \rrbracket_i$ to P_j , it also sends $\llbracket \llbracket y \rrbracket_i \cdot \Delta_j \rrbracket_i$
- Suppose P_i wants to send some $\llbracket y' \rrbracket_i$ instead. P_i knows $d = \llbracket y' \rrbracket_i - \llbracket y \rrbracket_i$
- In order to pass, P_i has to change the MAC share by $\Delta_j \cdot d$
- If P_i is successful, then Δ_j can be obtained from his knowledge

BeDOZA style MACs (cont.)

- Cheating against P_i is at least as hard as guessing a random element of \mathbb{Z}_q
- How hard is this? Depends on q
- If q is small, then we just use more Δ -s
 - Instead of Δ_i , have $\Delta_{i,1}, \dots, \Delta_{i,k}$, s.t. q^k is sufficiently large
- If several different q -s are used in the protocol, then have different Δ -s for each of them

MACs in computations

- MACs are linear. Linear operations are still local
- Correlated randomness includes MACs
- If some P_i needs to enter a new value v into the computation:
 - Correlated randomness includes a random $\langle\langle r \rangle\rangle$ (with all the MACs)
 - Open r to P_i
 - P_i sends $\delta = v - r$ to everybody
 - Take $\langle\langle v \rangle\rangle \leftarrow \langle\langle r \rangle\rangle + \delta$

Communication complexity of MACs

- MAC shares are included in correlated randomness. This gets bigger
- MAC shares are communicated in the online phase
 - In general, many values are sent in each round. Each has accompanying MAC share(s)
- But the receiver already knows, what he is supposed to receive
- Do not send the shares themselves. Send their hash
- I.e. The online communication complexity is minimal

MACs for multiplications and high bits

- Correlated randomness also gets MACs
- The operations in the title, together with correlated randomness, are “linear” operations
- Other “linear” operations:
 - Characteristic vector
 - equality check
 - Generation of random bits

MACs for less-than

- Operation: $\llbracket w \rrbracket_i \mapsto (\llbracket v_0 \rrbracket_i, \llbracket v_1 \rrbracket_i, \dots, \llbracket v_{n-1} \rrbracket_i)$
- $\llbracket w \rrbracket_i$ has MAC for party P_{1-i} . How do we get MACs on $\llbracket v_j \rrbracket_i$?

MACs for less-than

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 - $s \in \mathbb{Z}_q$, known to party 0 only
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 - $\langle\!\langle d_0 \rangle\!\rangle, \dots, \langle\!\langle d_{n-1} \rangle\!\rangle$, for digits d_0, \dots, d_{n-1} of s

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- P_0 sends $t = \llbracket w \rrbracket_0 - s$ to P_1
 - With MAC check
- P_0 updates $\llbracket w \rrbracket_0 := s$
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- P_0 updates $\llbracket w \rrbracket_0 := s$
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- Now we have MACs on P_0 's shares of digits of $\llbracket w \rrbracket$
- Same MACs work for P_0 's shares of digits of $\llbracket w \rrbracket - \llbracket c \rrbracket$
- Other details of “less than” also support MACs on P_0 's shares

Active security against one party only

- Rejection check is actively secure against phone, passively secure against server
 - It is actively *private* against server
- If checks pass, then the signature will be revealed to phone only
- Phone verifies the signature. If bad, then server has cheated

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- Server's best strategy to learn something: follow the protocol

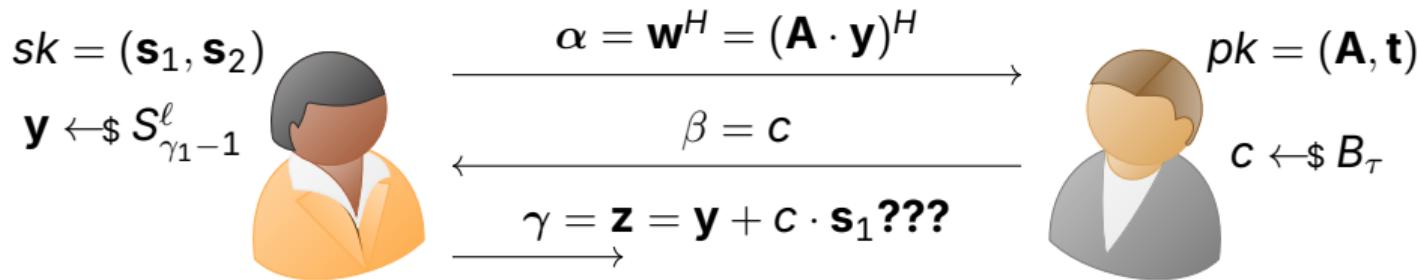
Source of correlated randomness?

- This randomness could be generated ahead-of-time
 - There are protocols for certain kinds of correlated randomness
 - These protocols are (more) expensive

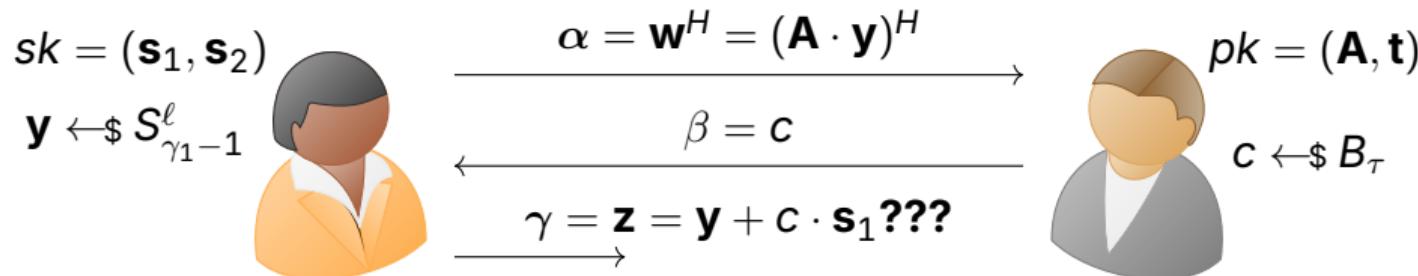
Source of correlated randomness?

- This randomness could be generated ahead-of-time
 - There are protocols for certain kinds of correlated randomness
 - These protocols are (more) expensive
- We just add an extra party
 - Randomness going to one of the parties (Phone) can be expanded from a single random seed
 - We keep that extra party close to the other party (Server)
 - But under independent control
- In a 3-party protocol (P and S and CRP), we can handle one malicious party

Making \mathbf{w}^H public? “Dilithium identification protocol”



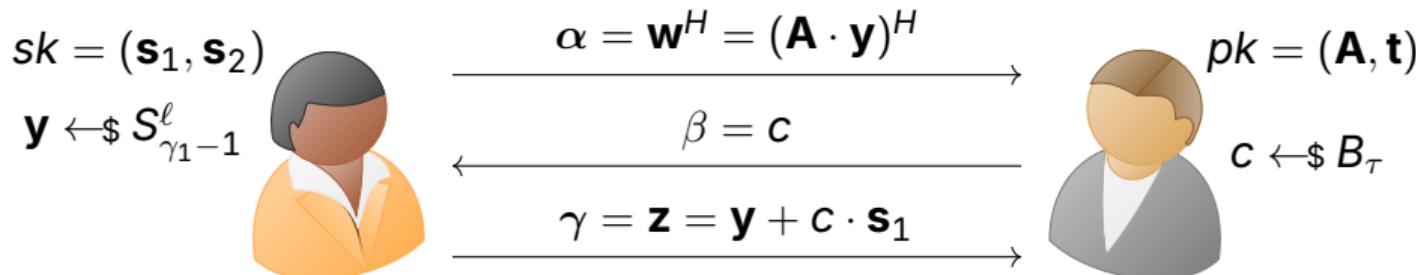
Making \mathbf{w}^H public? “Dilithium identification protocol”



Alice checks:

- Would the value of $\mathbf{y} + c \cdot \mathbf{s}_1$ be achievable for all possible $\tilde{\mathbf{s}}_1 \in R_1^n$?
 - Is it the case that $\forall \tilde{\mathbf{s}}_1 \in S_\eta^\ell \exists \tilde{\mathbf{y}} \in S_{\gamma_1-1}^\ell$, s.t. $\mathbf{y} + \beta \cdot \mathbf{s}_1 = \tilde{\mathbf{y}} + c \cdot \tilde{\mathbf{s}}_1$?
- Would the value of $\mathbf{w}^L - c \cdot \mathbf{s}_2$ be achievable for all possible $\tilde{\mathbf{s}}_2 \in R_1^m$?
- I.e. aren't these values “dangerously off to one side”?

Making \mathbf{w}^H public? “Dilithium identification protocol”

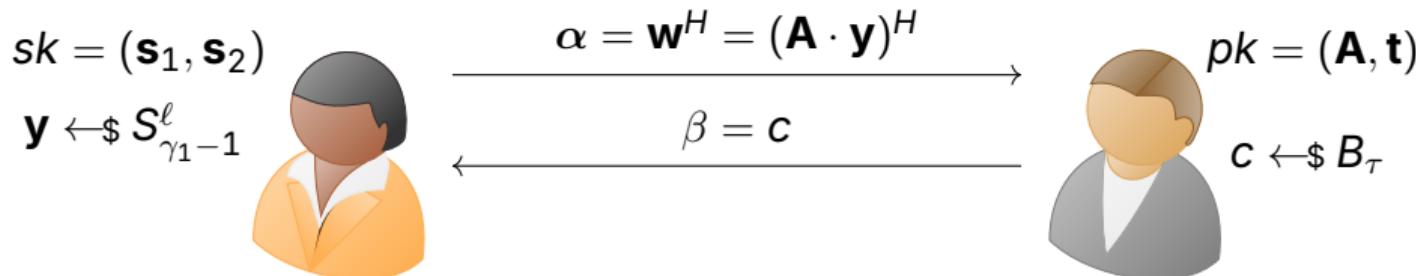


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NO

Making w^H public? “Dilithium identification protocol”

$$sk = (\mathbf{s}_1, \mathbf{s}_2)$$

$$\mathbf{y} \leftarrow \$ S_{\gamma_1-1}^\ell$$



no-abort Honest Verifier
Zero-Knowledge

Bob forgets that this
interaction took place at all

$$pk = (\mathbf{A}, \mathbf{t})$$

$$c \leftarrow \$ B_\tau$$



Alice checks:

- Would the value of $\mathbf{y} + c \cdot \mathbf{s}_1$ be achievable for all possible $\tilde{\mathbf{s}}_1 \in R_1^n$?
 - Is it the case that $\forall \tilde{\mathbf{s}}_1 \in S_\eta^\ell \exists \tilde{\mathbf{y}} \in S_{\gamma_1-1}^\ell$, s.t. $\mathbf{y} + \beta \cdot \mathbf{s}_1 = \tilde{\mathbf{y}} + c \cdot \tilde{\mathbf{s}}_1$?
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NO

Discussion

- Cozzo & Smart [IMACC 2019]: should compute $H(\mu, \mathbf{w}^H)$ under MPC
- Barthe et al. [EC 2018]: introduce a hardness assumption to open \mathbf{w}^H
- Problem occurs also in hardware security
 - Migliore et al. [ACNS 2019] ignore it
 - Coron et al. [TCHES 2023(4)] [TCHES 2024(4)] and Azouaoui et al. [TCHES 2023(4)] recognize it
 - “Future work”, “need a hardness assumption”, “conjecture that not a problem”
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- We modify Bienstock et al.’s argument, use MLWR on unmodified Dilithium
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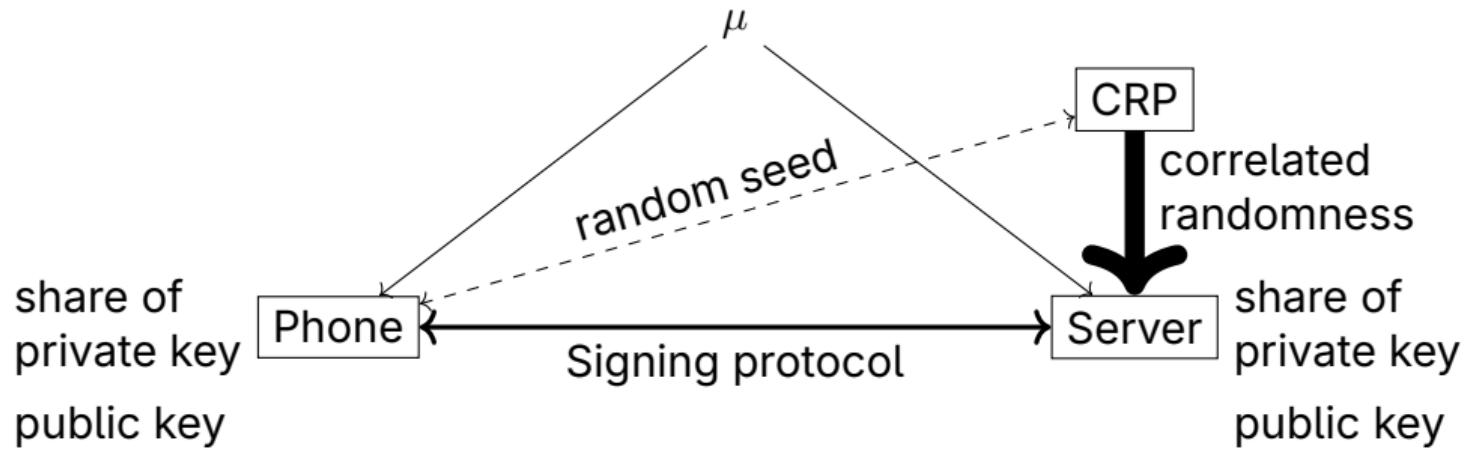
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- Azouaoui et al. [TCHES 2023(4)]

When the boundary checks do not pass, the reduction from LWR to LWE does not apply immediately since the distribution on \mathbf{w}_1 changes slightly. Leaving \mathbf{w}_1 unmasked therefore requires the additional explicit assumption that the corresponding LWR problem is hard. Since the number of rounded bits is significantly higher than the error that is added in the LWE problem, we conjecture that \mathbf{w}_1 does not require side-channel countermeasures. The same expectation was also shared by Vadim Lyubashevky in a personal communication and publicly during RWPQC23. To be conservative, we nevertheless study the option

- We modify Bienstock et al.'s argument, use MLWR on unmodified Dilithium
 - Hopefully the argument can be improved. See [ePrint 2025/675]

Benchmarking set-up



Network traffic

Key generation: traffic volume

| ML-DSA- | P↔S | CRP→S |
|---------|-------------|----------|
| -44 | 920.52 KiB | 1.42 MiB |
| -65 | 1760.22 KiB | 2.66 MiB |
| -87 | 3704.24 KiB | 4.44 MiB |

Signing attempt: traffic volume

| ML-DSA- | P↔S | CRP→S |
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| -44 | 186.72 KiB | 55.05 MiB |
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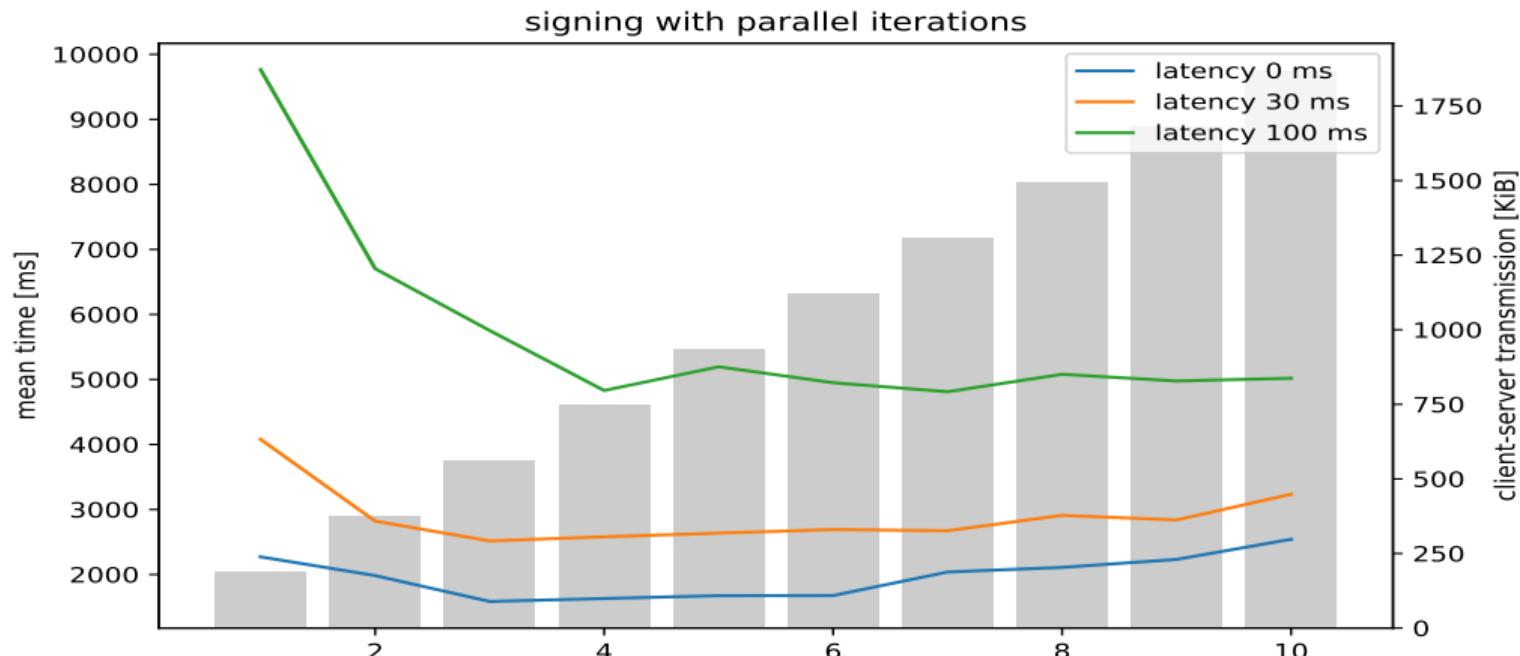
Key generation: num. of rounds

3

Signing attempt: num. of rounds

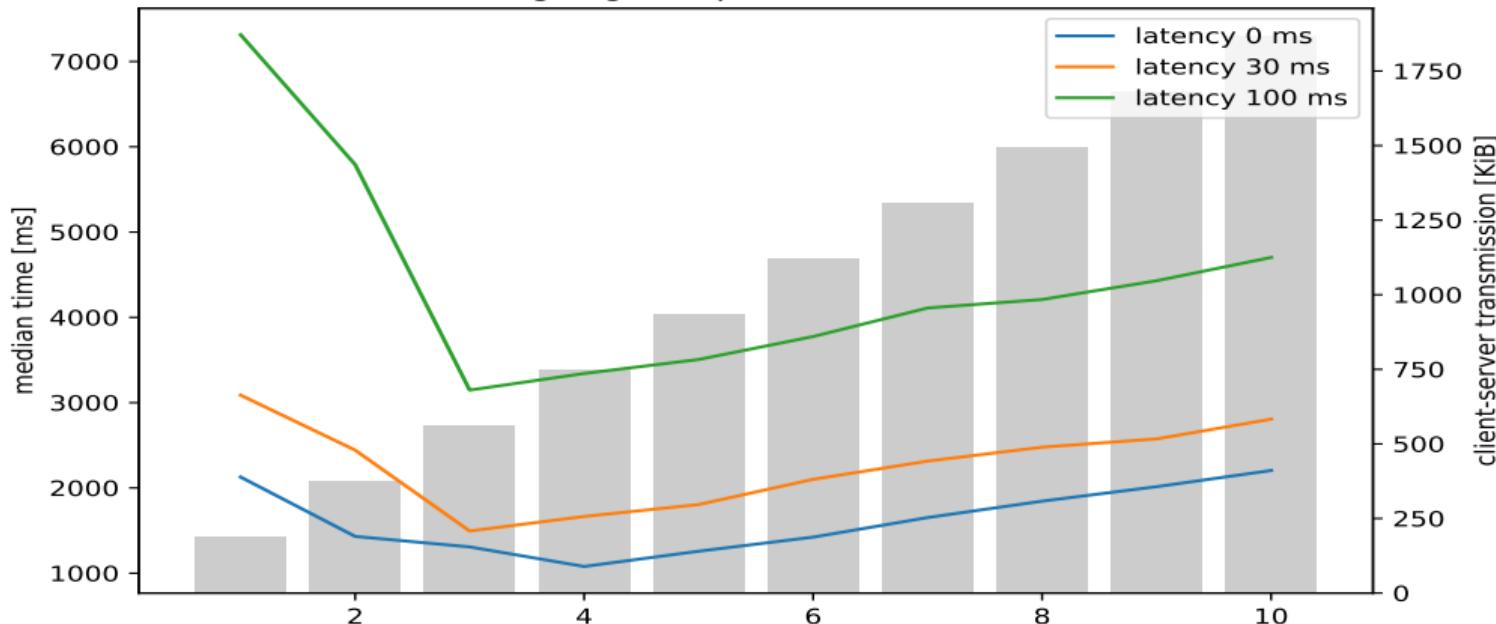
14

Time to create a signature (mean)

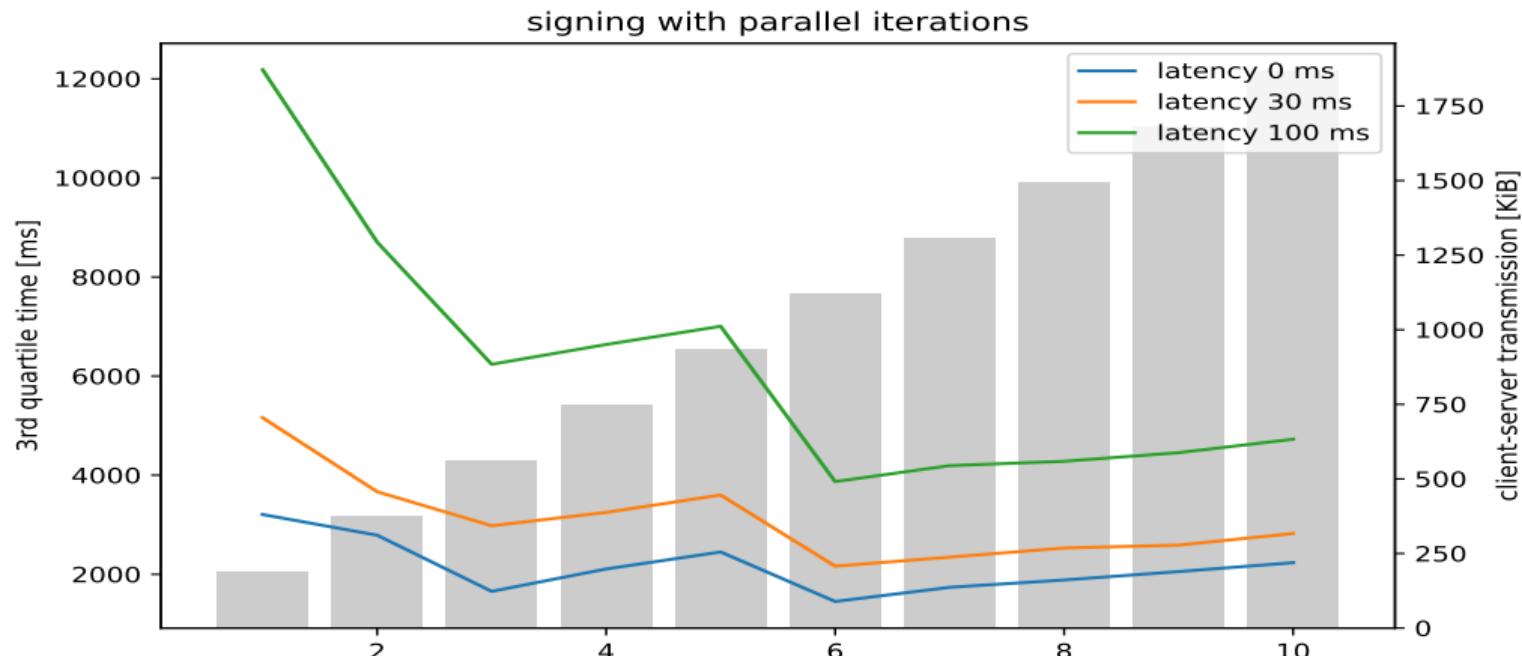


Time to create a signature (median)

signing with parallel iterations



Time to create a signature (3rd quartile)



Conclusion

- We obtain our desiderata (two-party, interchangeable with a standardized scheme, passes known answer tests)
- The online complexity is good enough
- The setting with an extra CRP may be challenging wrt. certification
 - But hopefully can be set up and presented in an approvable manner
- The amount of correlated randomness is a bit too high
 - But probably not show-stoppingly high